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\*\* CONTINUING DATA \*\*\*\*\*

None TT

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

None TT

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY VT	SHEETS DRAWING 3	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged Examiner's Signature Initials				

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## TITLE

DEFECT DIAGNOSIS FOR SEMICONDUCTOR INTEGRATED CIRCUITS

☐ All Fees☐ 1.16 Fees ( Filing )